| L<br>Number | Hits   | Search Text   | DB        | Time stamp          |
|-------------|--------|---|-----------|---------------------|
| 1           | 432    | 438/96.ccls.  | USPAT     | 2004/09/29<br>17:33 |
| 2 .         | 264    | 438/97.ccls.  | USPAT     | 2004/09/29          |
| 3           | 682    | 438/149.ccls.   | USPAT     | 17:33<br>2004/09/29 |
| 4           | 100    | 438/150.ccls.   | USPAT     | 17:33<br>2004/09/29 |
| 5           | 661    | 438/151.ccls.   | USPAT     | 17:34<br>2004/09/29 |
| 6           | 641    | 438/166.ccls.   | USPAT     | 17:34<br>2004/09/29 |
| 7 .         | 480    | 438/479.ccls.   | USPAT     | 17:34<br>2004/09/29 |
| 8           | 251    | 438/482.ccls.   | USPAT     | 17:34<br>2004/09/29 |
| 9           | 385    | 438/486.ccls.   | USPAT     | 17:34<br>2004/09/29 |
| 10          | 347    | 438/487.ccls.   | USPAT     | 17:34<br>2004/09/29 |
| 11          | 381    | 438/488.ccls.   | USPAT     | 17:34<br>2004/09/29 |
| 12          | 83     | 438/491.ccls.   | USPAT     | 17:34<br>2004/09/29 |
| 13          | 75     | lee-seok\$.in.  | USPAT     | 17:35<br>2004/09/29 |
| 14          | 91     | lee-seok\$.in.  | US-PGPUB  | 17:35<br>2004/09/29 |
| 15          | 48     | lee-seok\$.in.  | EPO; JPO; | 17:35<br>2004/09/29 |
|             | ~      | iee-seoro.iii.  | DERWENT;  | 17:35               |
| 16          | 10249  | (amorph\$6 and crystal\$9) and (anneal\$6 rta<br>rtp rto) and (silicon polysilicon)   | USPAT     | 2004/09/29<br>17:41 |
| 17          | . 4160 | ((amorph\$6 and crystal\$9) and (anneal\$6<br>rta rtp rto) and (silicon polysilicon)) and<br>(TFT (thin same transistor))   | USPAT     | 2004/09/29<br>17:41 |
| 18          | 7700   | ((amorph\$6 and crystal\$9) and (anneal\$6<br>rta rtp rto) and (silicon polysilicon)) and<br>(amorph\$6 same crystal\$9)  | USPAT     | 2004/09/29<br>17:38 |
| 19          | 3406   | ( ((amorph\$6 and crystal\$9) and (anneal\$6<br>rta rtp rto) and (silicon polysilicon)) and<br>(amorph\$6 same crystal\$9) ) and<br>(((amorph\$6 and crystal\$9) and (anneal\$6   | USPAT     | 2004/09/29<br>17:39 |
| 20          | 2675   | rta rtp rto) and (silicon polysilicon)) and (TFT (thin same transistor))) (( ((amorph\$6 and crystal\$9) and (anneal\$6 rta rtp rto) and (silicon polysilicon)) and (amorph\$6 same crystal\$9) ) and (((amorph\$6 and crystal\$9) and (anneal\$6 | USPAT     | 2004/09/29<br>17:39 |
|             |        | rta rtp rto) and (silicon polysilicon)) and (TFT (thin same transistor)))) and active   |           |                     |

| 21 | 2380 | ((( ((amorph\$6 and crystal\$9) and (anneal\$6 | USPAT     | 2004/09/29 |
|----|------|--|-----------|------------|
|    |      | rta rtp rto) and (silicon polysilicon)) and    |           | 17:39      |
|    |      | (amorph\$6 same crystal\$9) ) and              |           |            |
|    |      | (((amorph\$6 and crystal\$9) and (anneal\$6    |           |            |
|    |      | rta rtp rto) and (silicon polysilicon)) and    |           |            |
|    |      | (TFT (thin same transistor)))) and active)     |           |            |
|    |      | and pattern\$6                                 |           |            |
| 22 | 170  | (((( ((amorph\$6 and crystal\$9) and           | USPAT     | 2004/09/29 |
|    |      | (anneal\$6 rta rtp rto) and (silicon           |           | 17:40      |
|    |      | polysilicon)) and (amorph\$6 same              |           | -          |
|    |      | crystal\$9) ) and (((amorph\$6 and crystal\$9) |           |            |
|    |      | and (anneal\$6 rta rtp rto) and (silicon       |           |            |
|    |      | polysilicon)) and (TFT (thin same              |           |            |
|    |      | transistor)))) and active) and pattern\$6) and |           |            |
|    |      | (rapid same oxid\$9)                           |           | 2.4        |
| 23 | 1609 | (((( ((amorph\$6 and crystal\$9) and           | USPAT     | 2004/09/29 |
|    |      | (anneal\$6 rta rtp rto) and (silicon           |           | 17:41      |
| •  |      | polysilicon)) and (amorph\$6 same              |           |            |
|    |      | crystal\$9) ) and (((amorph\$6 and crystal\$9) |           |            |
|    |      | and (anneal\$6 rta rtp rto) and (silicon       |           |            |
|    |      | polysilicon)) and (TFT (thin same              |           |            |
|    |      | transistor)))) and active) and pattern\$6) and |           |            |
|    |      | pixel  |           |            |
| 24 | 3968 | (amorph\$6 same crystal\$9) and (anneal\$6     | US-PGPUB  | 2004/09/29 |
|    |      | rta rtp rto) and (silicon polysilicon)         |           | 17:42      |
| 25 | 2062 | ((amorph\$6 same crystal\$9) and (anneal\$6    | US-PGPUB  | 2004/09/29 |
|    |      | rta rtp rto) and (silicon polysilicon)) and    |           | 17:42      |
|    |      | (TFT (thin same transistor))                   |           |            |
| 26 | 1451 | (((amorph\$6 same crystal\$9) and (anneal\$6   | US-PGPUB  | 2004/09/29 |
|    |      | rta rtp rto) and (silicon polysilicon)) and    |           | 17:42      |
|    |      | (TFT (thin same transistor))) and pixel        |           | •          |
| 27 | 1108 | (amorph\$6 same crystal\$9) and (anneal\$6     | EPO; JPO; | 2004/09/29 |
|    |      | rta rtp rto) and (silicon polysilicon)         | DERWENT;  | 17:42      |
|    |      |  | IBM_TDB   |            |
| 28 | 369  | ((amorph\$6 same crystal\$9) and (anneal\$6    | EPO; JPO; | 2004/09/29 |
|    |      | rta rtp rto) and (silicon polysilicon)) and    | DERWENT;  | 17:42      |
|    |      | (TFT (thin same transistor))                   | IBM TDB   | 1          |